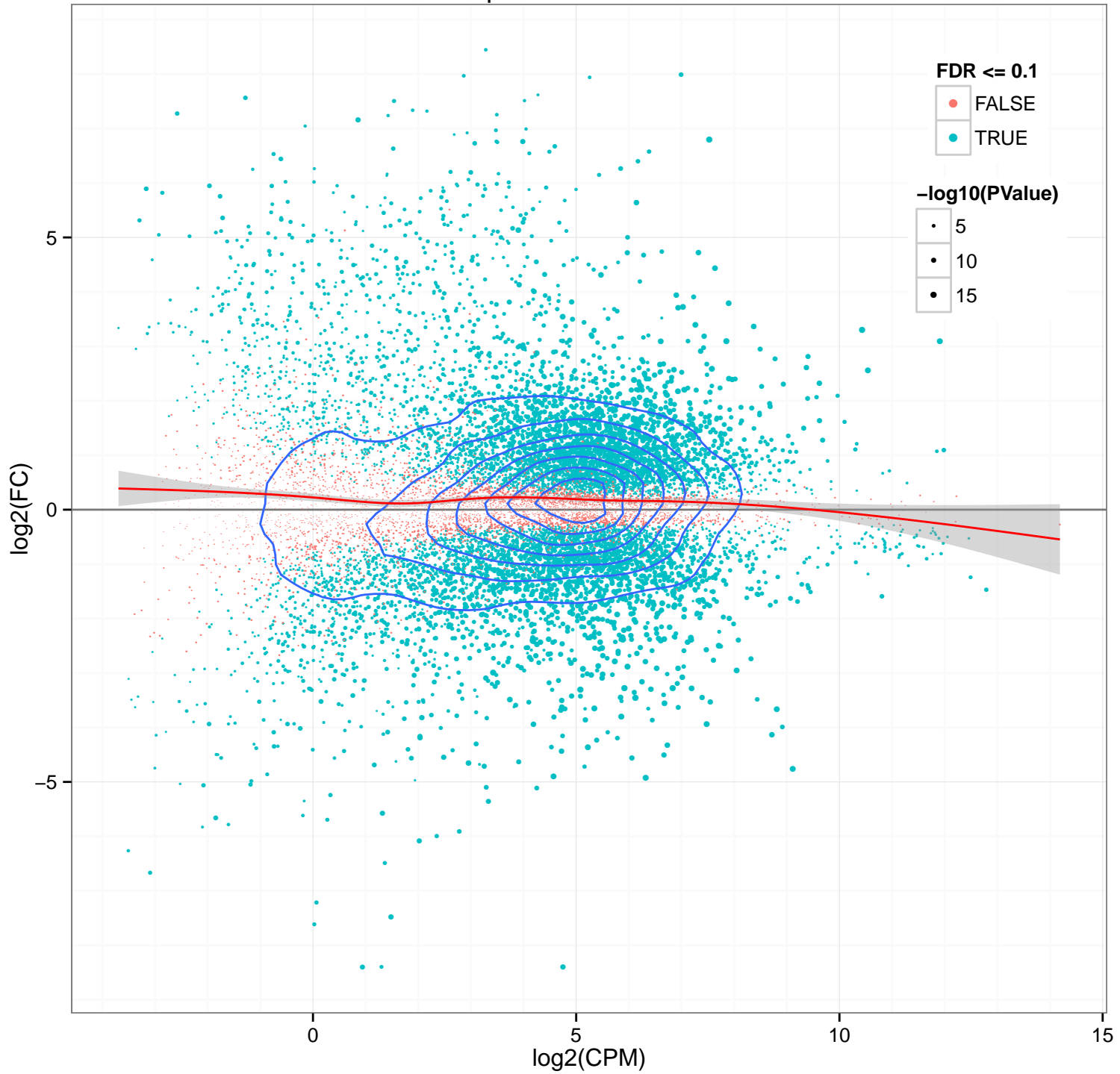
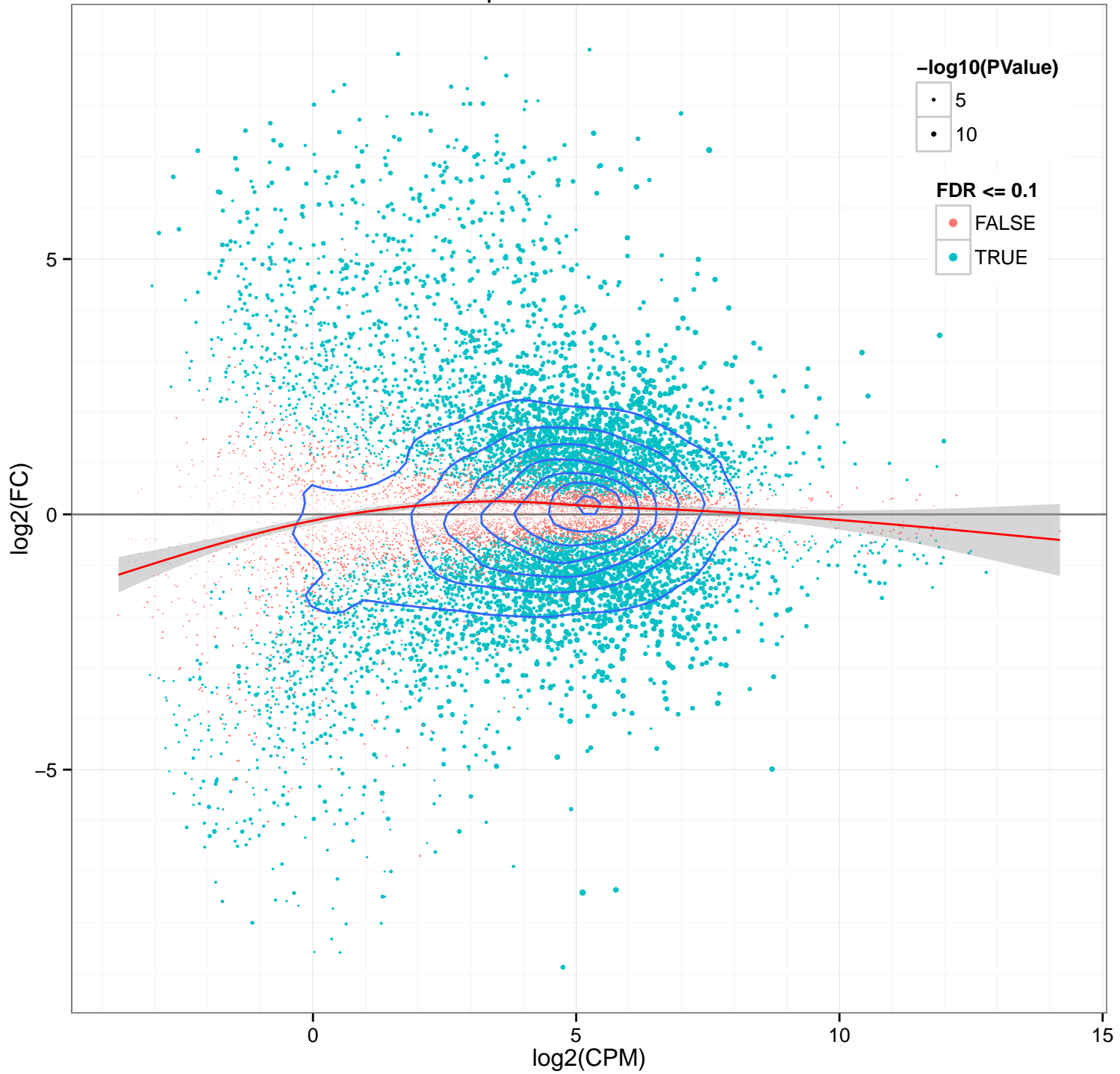


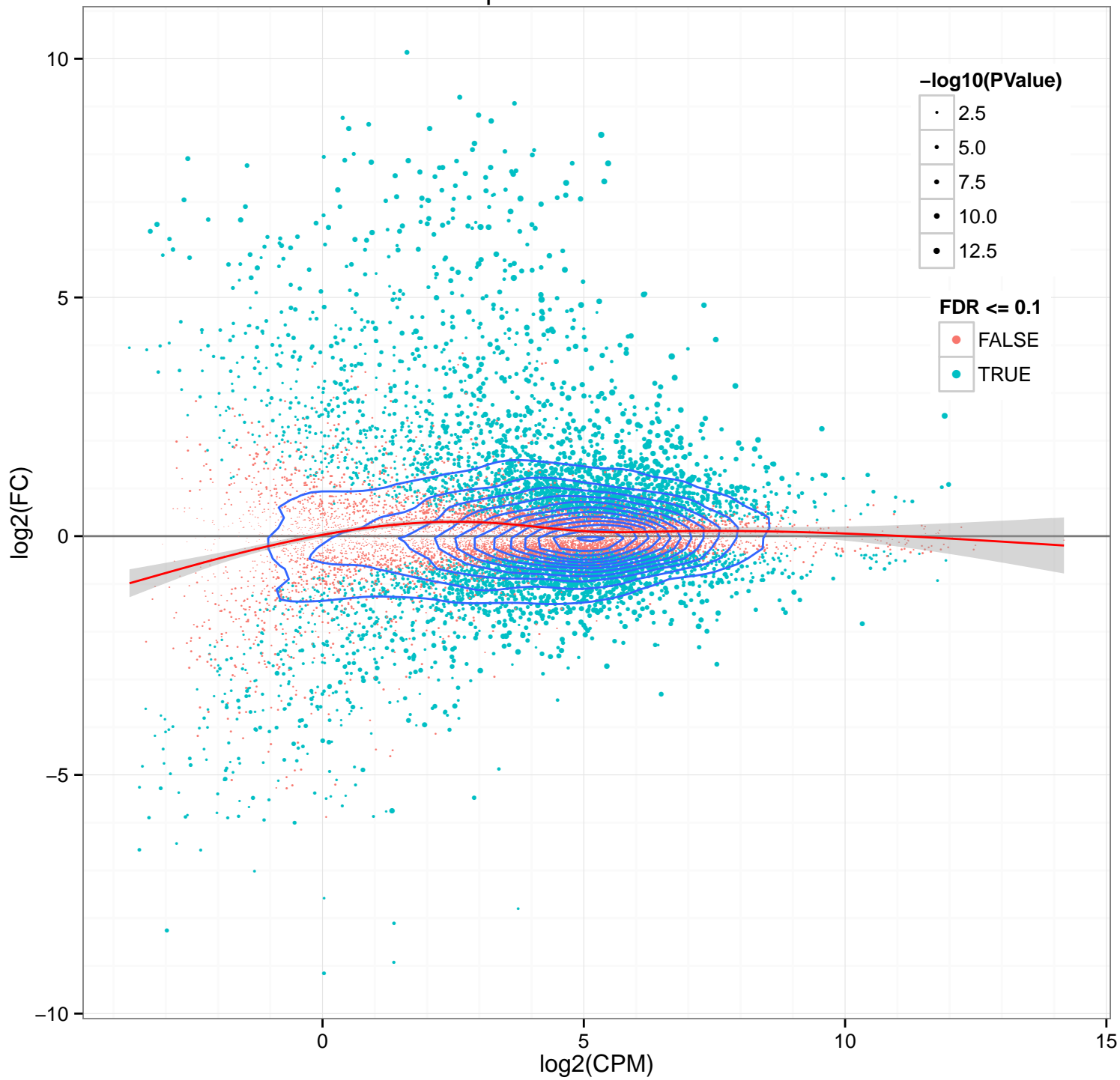
MA plot for Naive.D0vD1



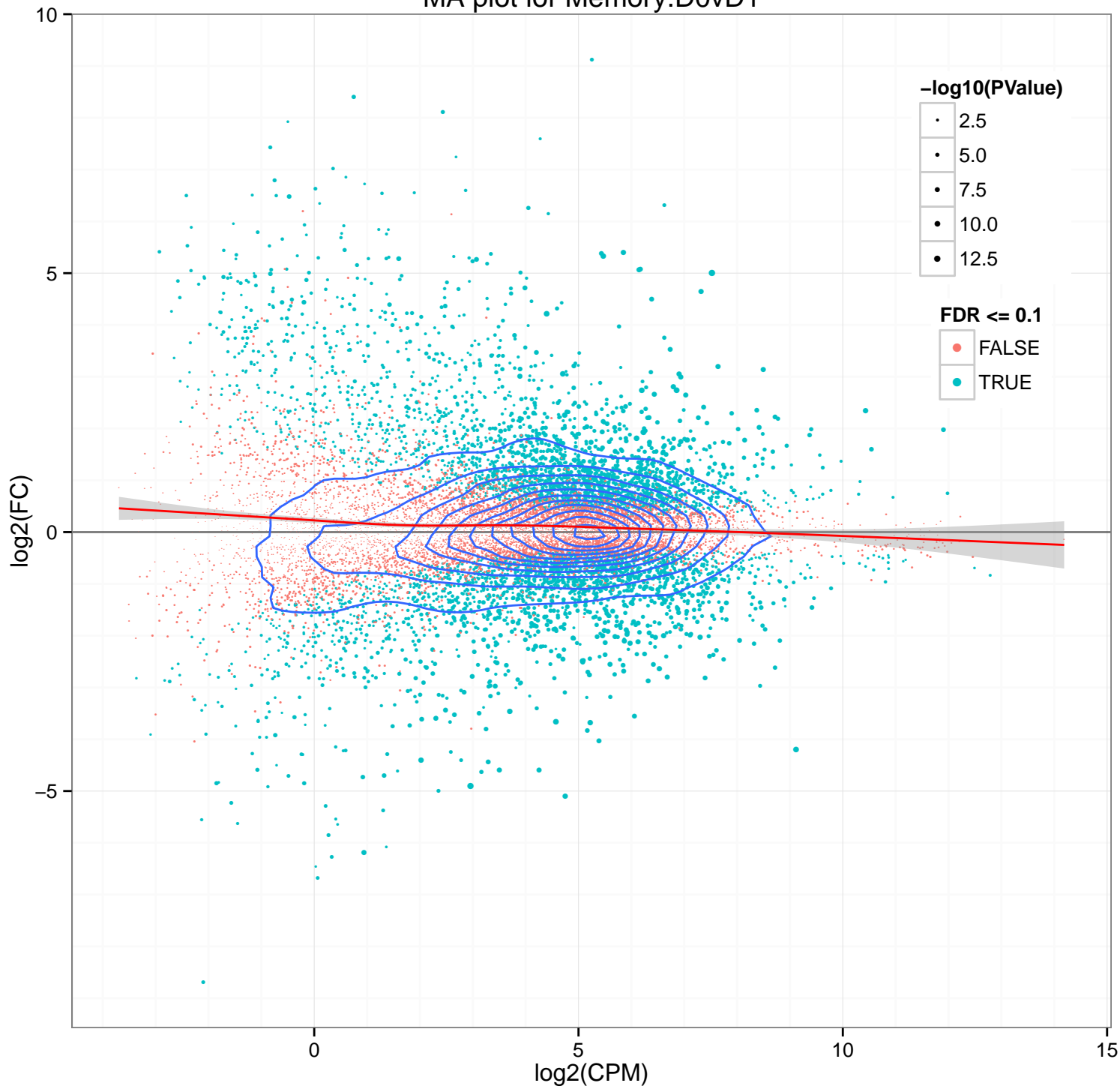
MA plot for Naive.D0vD5



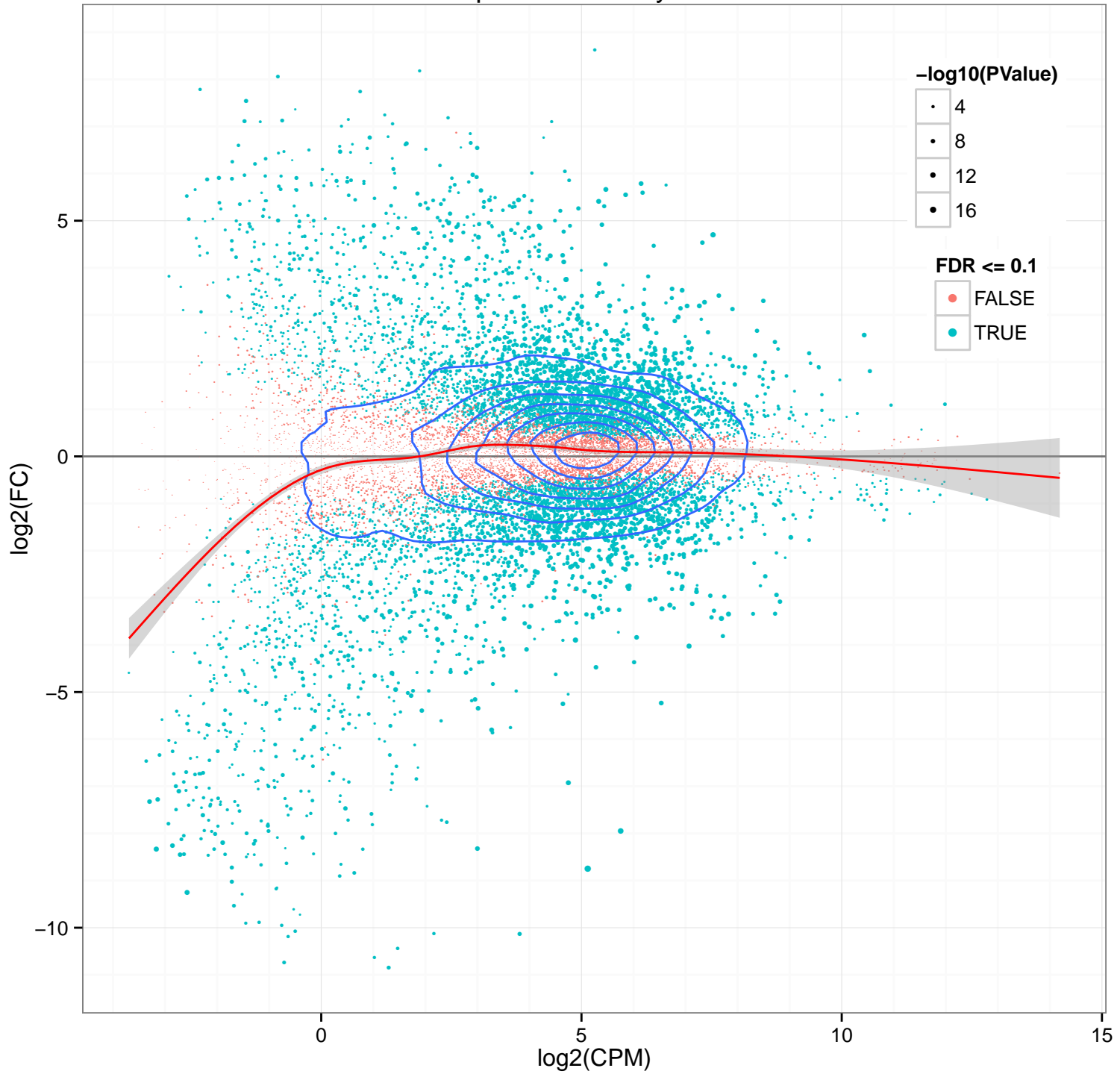
MA plot for Naive.D0vD14



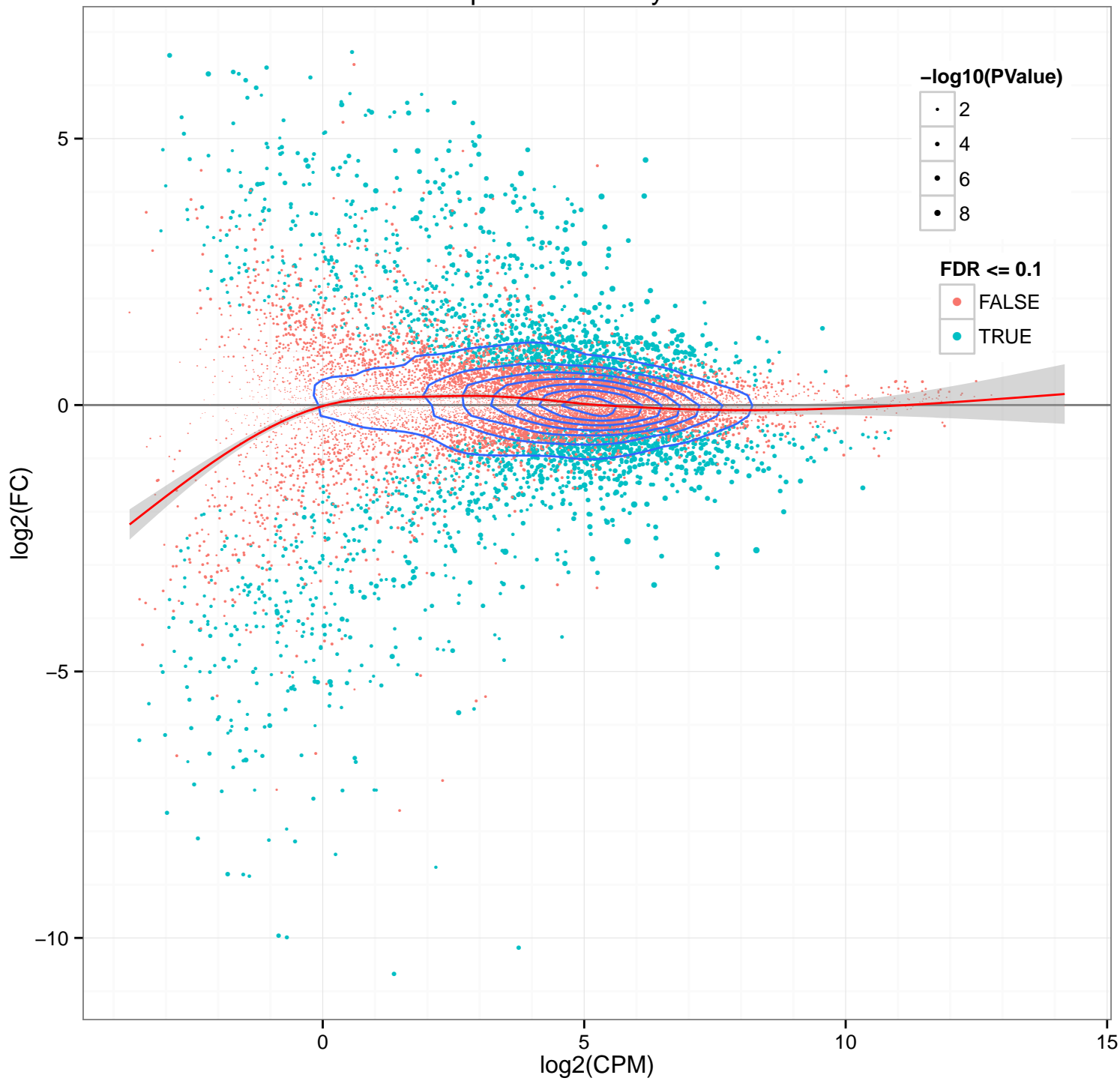
MA plot for Memory.D0vD1



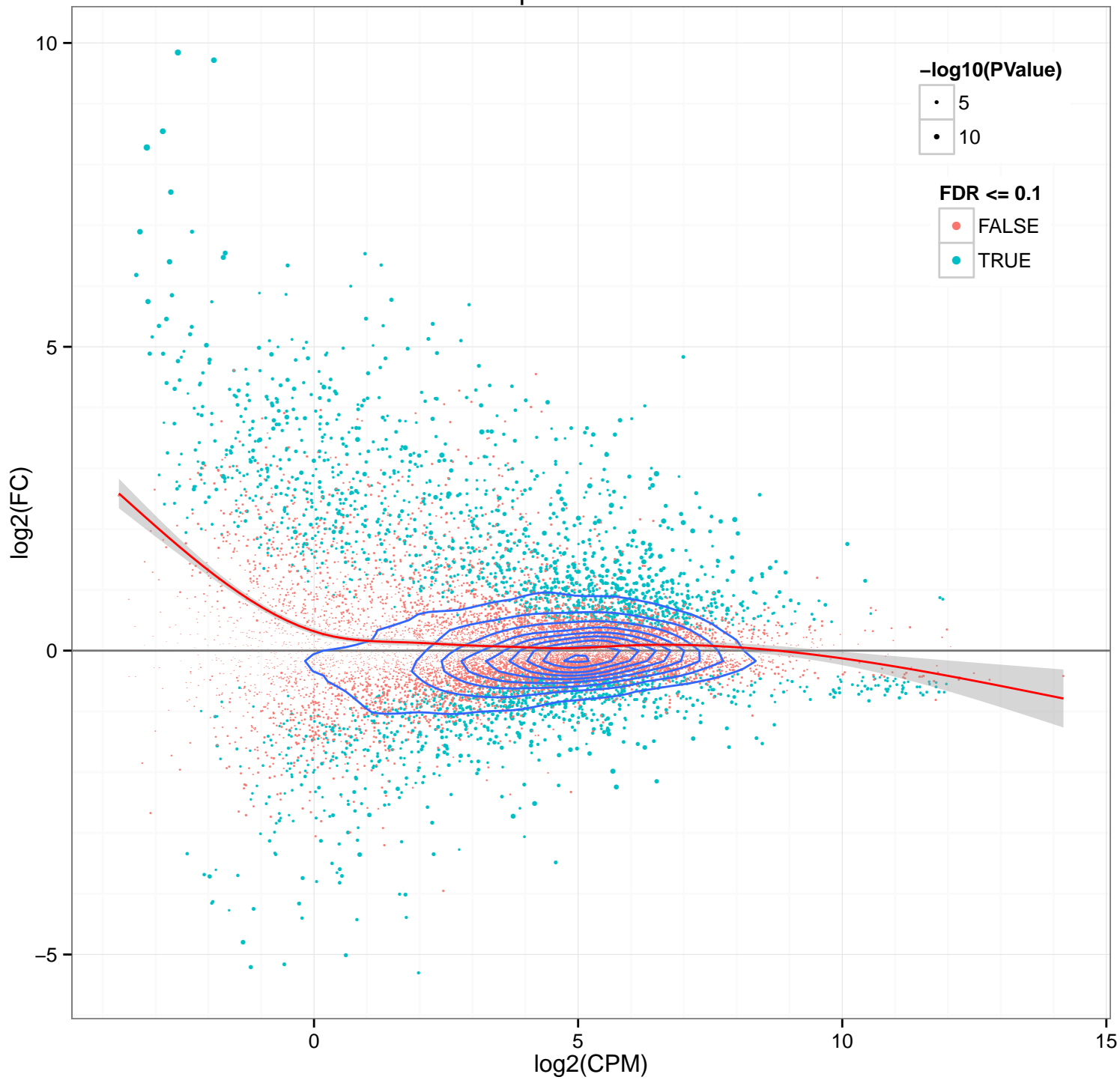
MA plot for Memory.D0vD5



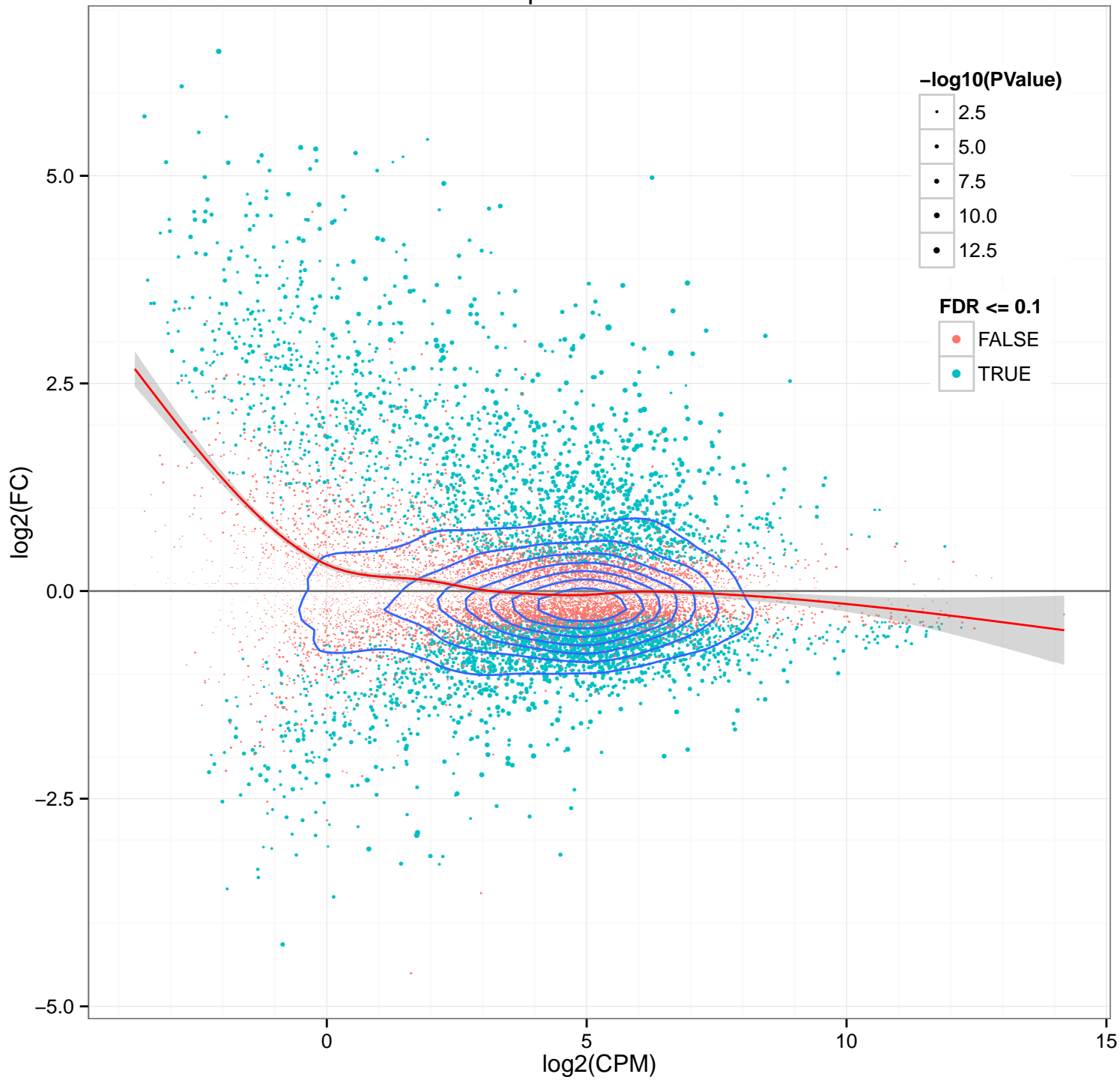
MA plot for Memory.D0vD14



MA plot for NvM.D0

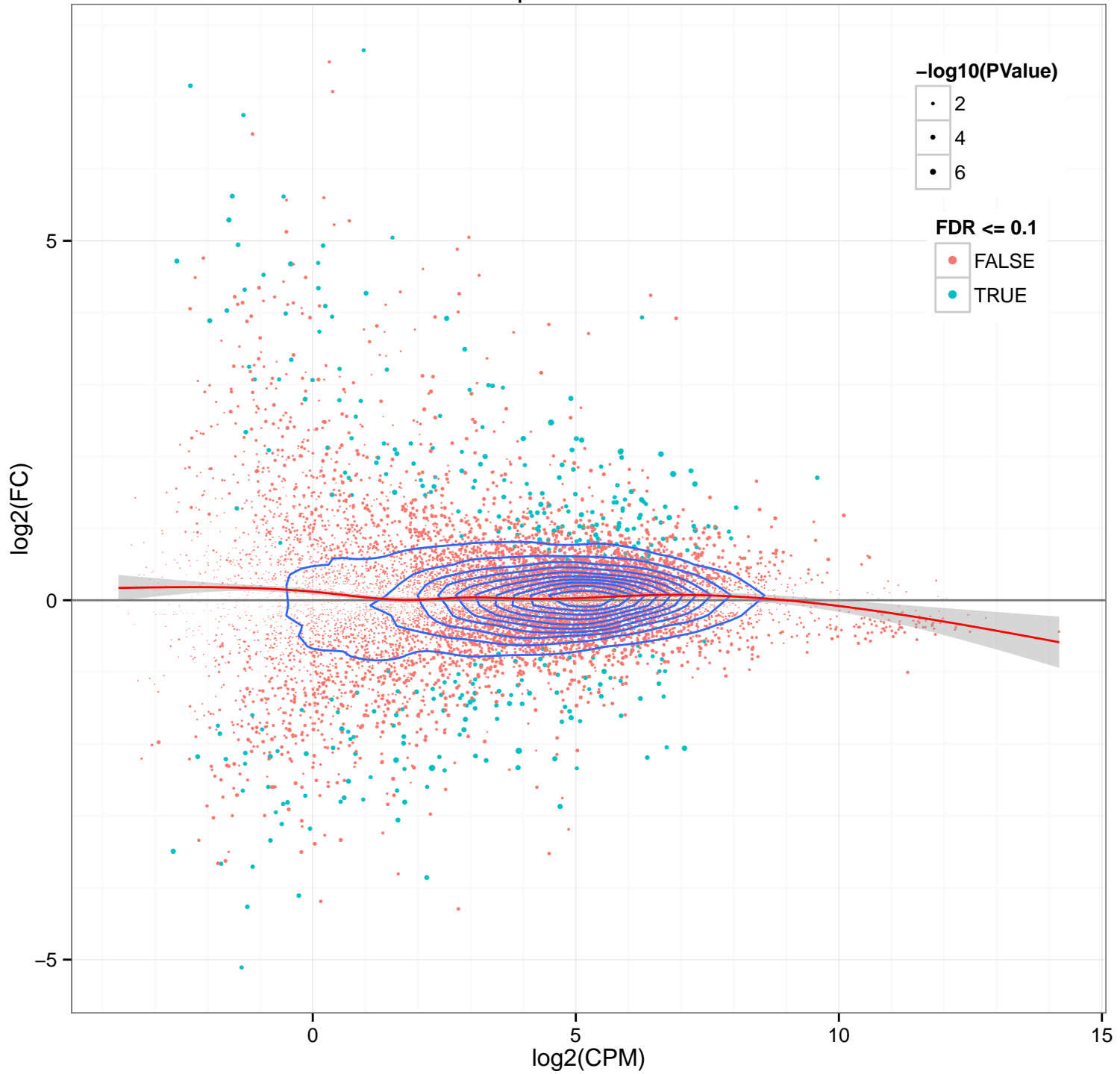


MA plot for NvM.D1

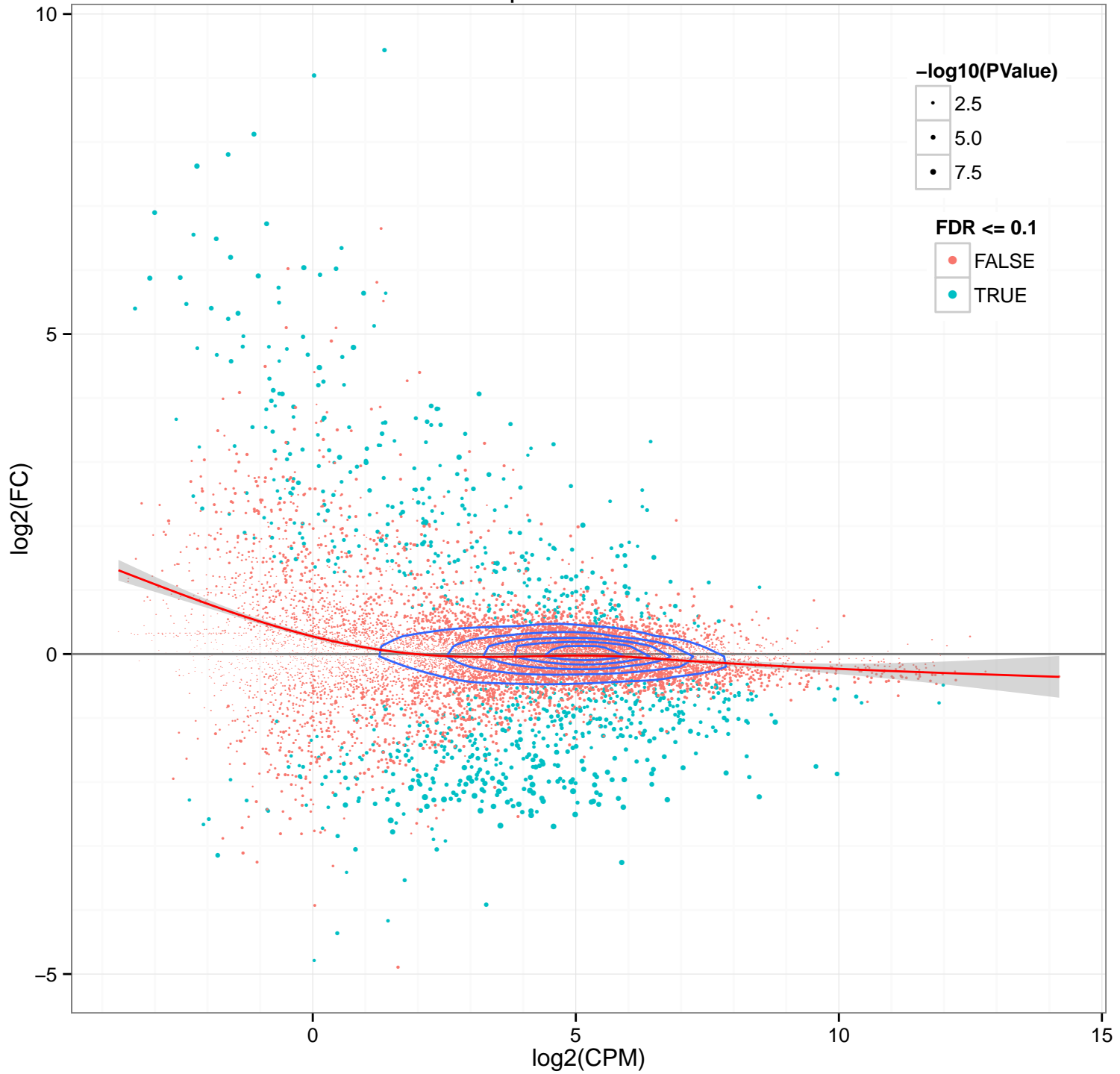




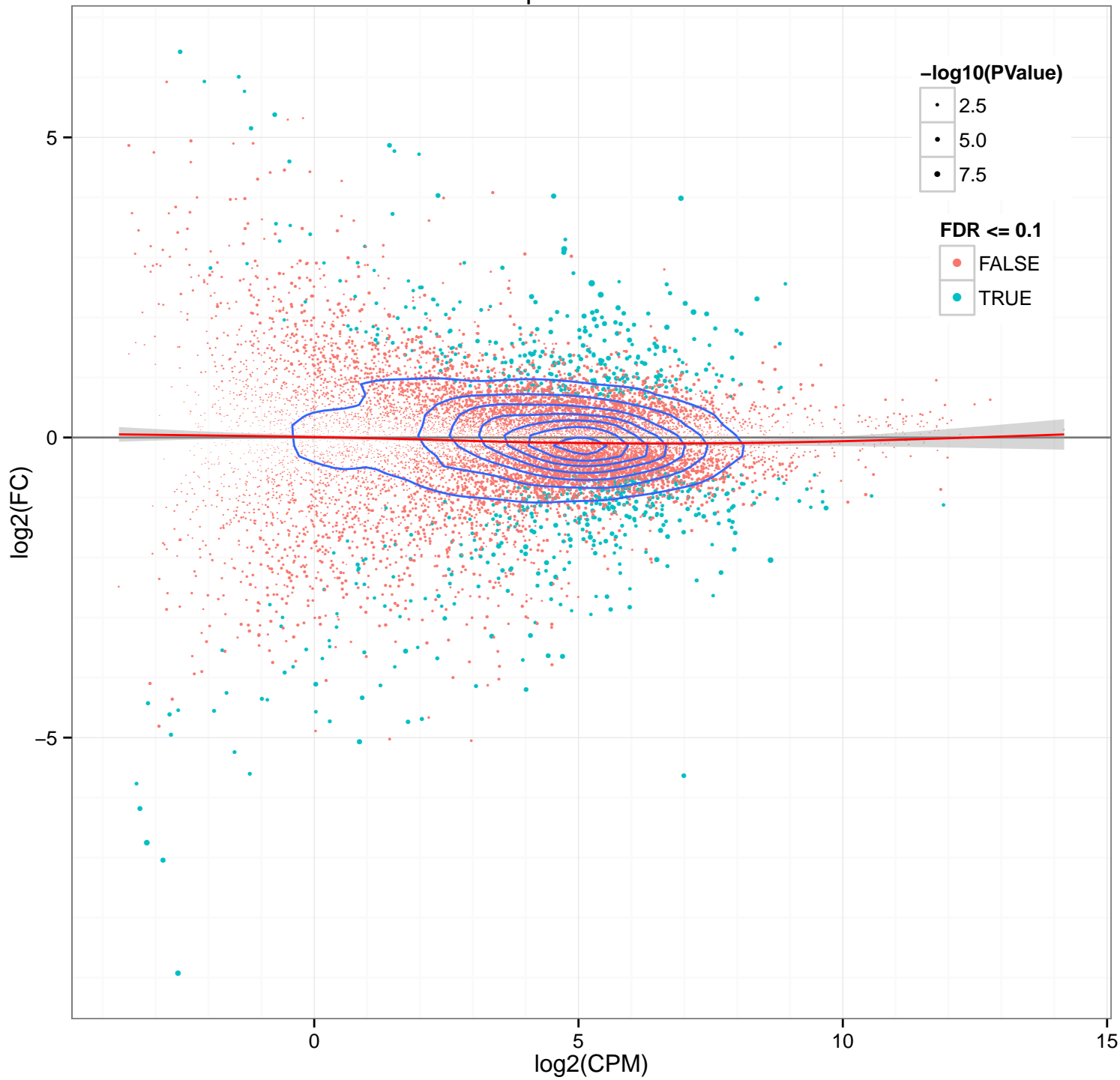
MA plot for NvM.D5



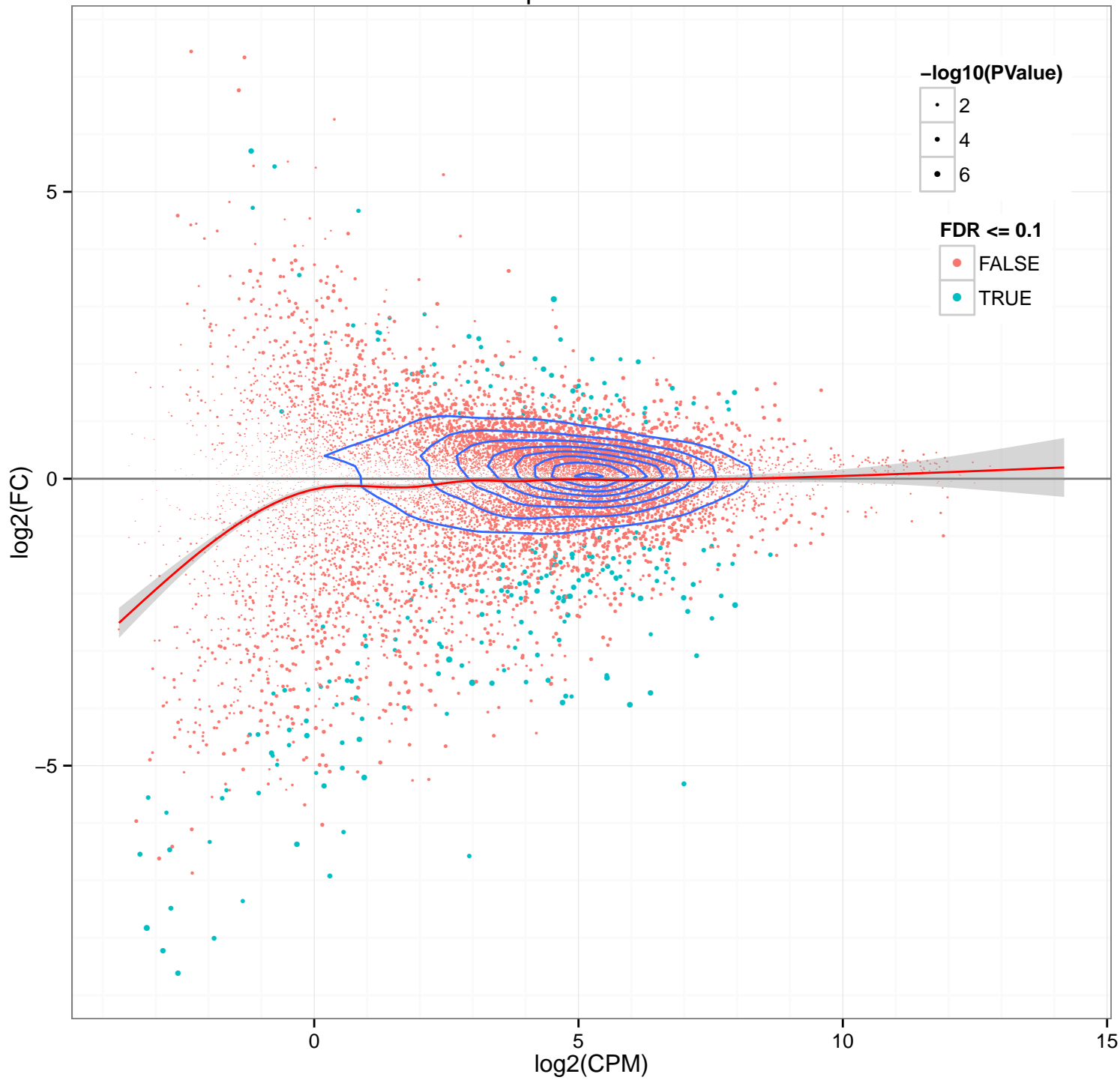
MA plot for NvM.D14



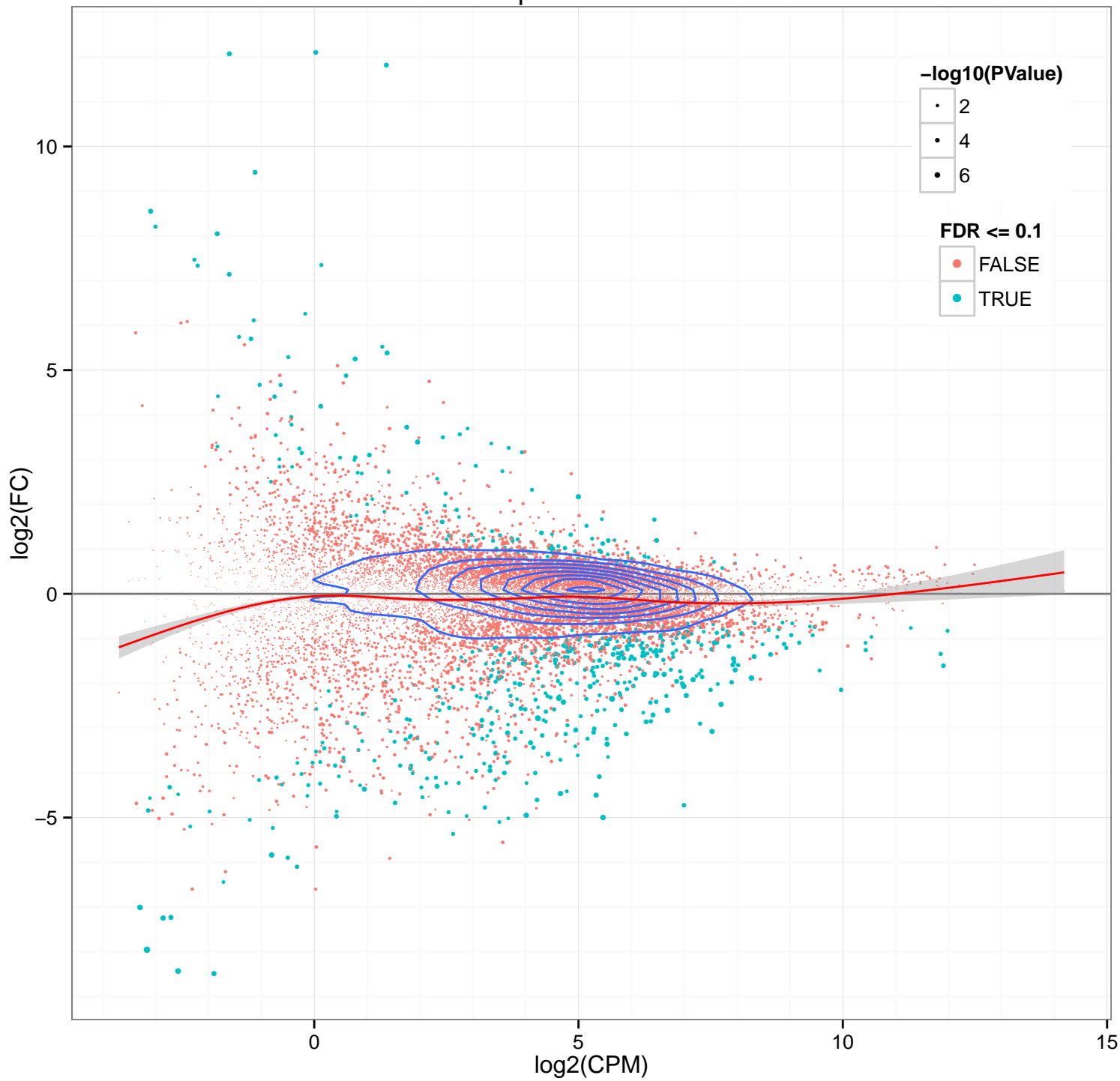
MA plot for Fac.D1



MA plot for Fac.D5



MA plot for Fac.D14



MA plot for MD0vND14

